ISO/IEC JTC1/SC17 2009-07-03
Cards and personal identification

ISO/IEC JTC1/SC17 N 3706 **WG8 N 1574 = TF2 N 639**

DOCUMENT TYPE: Notification of Ballot

TITLE: Notification of Ballot - ISO/IEC 14443-3:2001/PDAM 5 - Identification cards - Contactless integrated circuit(s) cards - Proximity cards — Part 3: Initialisation and anticollision — Part 3: Amendment 5: Activation of higher layer protocols

BACKWARD POINTER:

SOURCE: SECRETARIAT ISO/IEC JTC1/SC17

STATUS: This ballot has been posted to the ISO Electronic balloting application and is available under the Balloting Portal, Committee Internal Balloting.

Simultaneous NP ballot, (17n3705).

ACTION ID: Vote

WORK ITEM: To be advised.

DUE DATE: 2009-10-04

DISTRIBUTION: P and L-Members of ISO/IEC JTC1/SC17
JTC1 Secretariat
ISO/IEC ITTF

MEDIUM: SERVER

NO. OF PAGES: 8

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ISO/IEC TC /SC 17 N

Date: 2009-07-1

ISO/IEC 14443-3:2001/PDAM 45

ISO/IEC TC /SC 17/WG 8

Secretariat: BSI

Identification cards - Contactless integrated circuit(s) cards - Proximity cards — Part 3: Initialisation and anticollision — Part 3: Amendment 4: Activation of higher layer protocols

Cartes d'identification — Cartes à circuit(s) intégrés sans contacts — Cartes de proximité — Partie 4:Protocole de transmission

Document type: International Standard
Document subtype: Amendment
Document stage: (30) Committee
Document language: E

O:\STANDARD\SC17\Work In Progress\WG8\fr NWIP 14443 amds\ISO-IEC_14443-3_A5_(E).doc STD Version 2.1c2

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Foreword

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The main task of technical committees is to prepare International Standards. Draft International Standards adopted by the technical committees are circulated to the member bodies for voting. Publication as an International Standard requires approval by at least 75 % of the member bodies casting a vote.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. ISO shall not be held responsible for identifying any or all such patent rights.

Amendment 5 to ISO/IEC 14443-3:2001 was prepared by Technical Committee ISO/IEC/TC , , Subcommittee SC 17, *Cards and Personal Identification*.

Introduction

Identification cards - Contactless integrated circuit(s) cards - Proximity cards — Part 3: Initialisation and anticollision — Part 3: Amendment 4: Activation of higher layer protocols

1 Page 35, section 7.7.4

Replace the Figure 22 by the following

b8	b7	b6	b5	b4	b3	b2	b1
RFU		X-bloc support	Extended ATQB supported	REQB / WUPB	N		

Figure 22 - Coding of PARAM

Before the Warning, add the sentence:

- b6 = 0 defines: X-bloc as define in ISO/IEC 14443-4 is not supported by the PCD,

- b6 = 1 defines: X-bloc as define in ISO/IEC 14443-4 is supported by the PCD.

Replace the sentence after the Warning by

A PCD sending a REQB/WUPB command with (b8 to b7) <> (00)b is not compliant with this standard.

The PICC should ignore (b8 to b7) and its interpretation of any other field of the whole frame shall not change.

2 Page 38, section 7.9.4

Replace the Figure 26 by the following

1st byte	2nd byte		3rd byte			4th byte (optional) Extended ATQB		
Bit_Rate_capability (8 bits)	Max_Frame_Size (4 bits)	Protocol_Type (4 bits)	FWI (4 bits)	ADC (2 bits)	FO (2 bits)	SFGI (4 bits)	X-bloc support (1 bit)	RFU (3 bits)

Figure 26 — Protocol Info format

3 Page 40, section 7.9.4.7

Replace the first sentence by:

The optional Extended ATQB byte (optional 4th byte of protocol info field) consists of three parts:

- bits (b3 to b1) are RFU and shall be set to (000)b;

- bit b4 define the X-block support as defined in ISO/IEC 14443-4 and shall be set to 1 if X-bloc are supported by the PICC

- the most significant half byte (b8 to b5) defines the Start-up Frame Guard time Integer (SFGI).